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Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)	
Office Action Summary		08/520,079	YAMAZAKI ET AL	•
		Examiner	Art Unit	
		Paul E. Brock II	2815	
The MAILING D. Period for Reply	ATE of this communication app	ears on the cover sheet wit	h the correspondence ad	dress
A SHORTENED STAT THE MAILING DATE Of the may be averafter SIX (6) MONTHS from the state of the period for reply specifiering the period for reply is specifiering. Failure to reply within the set	CUTORY PERIOD FOR REPLY OF THIS COMMUNICATION. ailable under the provisions of 37 CFR 1.13 the mailing date of this communication. d above is less than thirty (30) days, a reply field above, the maximum statutory period w or extended period for reply will, by statute, ice later than three months after the mailing int. See 37 CFR 1.704(b).	6(a). In no event, however, may a re within the statutory minimum of thirty ill apply and will expire SIX (6) MONT cause the application to become ABA	pply be timely filed r (30) days will be considered timely FHS from the mailing date of this co ANDONED (35 U.S.C. § 133).	y. ommunication.
Status				
2a)⊠ This action is FII 3)□ Since this applic	ommunication(s) filed on <u>24 Ja</u> NAL. 2b)∐ This ation is in condition for allowan ance with the practice under <i>E</i>	action is non-final. ce except for formal matte		e merits is
Disposition of Claims				
4a) Of the above 5) ☐ Claim(s) i 6) ☑ Claim(s) <u>73-116</u> , 7) ☐ Claim(s) i	123-141 and 143-155 is/are per claim(s) is/are withdraw s/are allowed. 123-141 and 143-155 is/are resolved to. are subject to restriction and/or	n from consideration. jected.		
Application Papers				
10) The drawing(s) fi Applicant may not Replacement drav	is objected to by the Examine led on 28 August 1995 is/are: request that any objection to the diving sheet(s) including the correction is objected to by the Examine.	a)⊠ accepted or b)⊡ obj drawing(s) be held in abeyand on is required if the drawing(ce. See 37 CFR 1.85(a). s) is objected to. See 37 CF	FR 1.121(d).
Priority under 35 U.S.C.	§ 119			
12) Acknowledgment a) All b) Som 1. Certified company 2. Copies of application	is made of a claim for foreign ne * c) None of: copies of the priority documents copies of the priority documents the certified copies of the prior n from the International Bureau detailed Office action for a list	s have been received. s have been received in Apity documents have been (PCT Rule 17.2(a)).	oplication No received in this National	Stage
	d (PTO-892) atent Drawing Review (PTO-948) tement(s) (PTO-1449 or PTO/SB/08)	Paper No(s	ummary (PTO-413))/Mail Date formal Patent Application (PTC 	O-152)

Art Unit: 2815

DETAILED ACTION

Information Disclosure Statement

1. Reference US-6,616,613 cited on the information disclosure statement filed August 18 2004 is a Patent to Goodman entitled "Physiological signal monitoring system" patented on September 9, 2003. This is believed to be an incorrect citation as the information disclosure statement credits Yamazaki et al. with this patent with a patent date of September 2, 2003. U.S. Patent 6,613,613 to Yamazaki on September 2, 2003 has been considered on the attached PTO-892.

Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. Claims 73 116, 123 –141, and 143 155 are rejected under 35 U.S.C. 103(a) as being unpatentable over Zhang et al. (USPAT 5563426, Zhang¹).

With regard to claim 73, Zhang¹ discloses in figure 4c a thin film transistor. Zhang¹ discloses in figures 1a – 1c, 2a - 2d, and 4a – 4c a crystalline semiconductor island (3) over a substrate (1a) having an insulating surface (1b). Zhang¹ discloses in figures 4b and 4c source (25a and 25c) and drain regions (25b and 25d) in said semiconductor island. Zhang¹ discloses in

Page 3

figure 4b a channel forming region (between 25a and 25b in figure 4b) between said source and drain regions. Zhang¹ discloses in figures 4a – 4c a gate insulating film (22) adjacent to at least said channel forming region. Zhang 1 discloses in figures 1a- 1c, 2a - 2d, and 4a - 4c a gate electrode (23a) adjacent to said channel forming region having said gate insulating film therebetween, wherein said channel forming region has no grain boundary (4). No differences have been pointed out in the formation of the channel forming region of Zhang¹ and the channel forming region of the current pending claim in view of the currently pending specification. Therefore Zhang¹ must teach in figures 1a - 1c, 2a - 2d and 4a - 4c wherein said semiconductor island includes a spin density not higher than 1 x 10¹⁷ cm⁻³, because an identical spin density is a property that must be shared by products that result from two processes that are the same. Zhang¹ discloses in figures 4c and column11, lines 47 – 56 wherein said crystalline semiconductor island includes hydrogen at concentration less than 1 x 10²⁰ cm⁻³ (i.e. the known atomic density of Si is 10^{22} cm⁻³, less than 5% of 10^{22} is less than 10^{20}). It is not clear if Zhang¹ teaches that the hydrogen concentration is not greater than 1 x 10²⁰ cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the hydrogen atom concentration of not greater than 1 x 10²⁰ cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. Further, any changes in particular device concentrations or properties would have been routine experimentation for one of ordinary skill seeking to maximize device function in the device of Zhang¹.

With regard to claim 74, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 wherein the crystalline semiconductor island comprise a material of Ni.

Art Unit: 2815

With regard to claim 75, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19-40 a thin film transistor wherein said material is included in said semiconductor island at a concentration less than 5×10^{19} cm⁻³. It is not clear if Zhang¹ teaches that the material is included in the semiconductor at a concentration not greater than 5×10^{19} cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the material included in the semiconductor at a concentration not greater than 5×10^{19} cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 76, Zhang¹ discloses in column 9, lines 38 – 45 a thin film transistor wherein said semiconductor island includes the point defect (oxygen) of less1 x 10¹⁸ cm⁻³. It is not clear if Zhang¹ teaches wherein said semiconductor island includes a point defect of 1 x 10¹⁶ cm⁻³ or more. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to have said semiconductor island include a point defect of 1 x 10¹⁶ cm⁻³ or more in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. Zhang¹ teaches in column 11, lines 47 – 56 that is obvious to have the hydrogen element for neutralizing the point defect at a concentration of 1 x 10¹⁸.

With regard to claim 77, it is obvious in Zhang¹ wherein said semiconductor island includes the spin density not lower than 1x10¹⁵ cm⁻³.

With regard to claim 78, Zhang¹ discloses in column 4, lines 18 – 20 wherein said semiconductor island is a silicon island.

With regard to claim 79, Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than 1 x

Art Unit: 2815

10¹⁸ cm⁻³, and oxygen at a concentration less than 1 x 10¹⁸ cm⁻³. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not lower than 1 x 10¹⁶ cm⁻³, and oxygen at a concentration not lower than 1 x 10¹⁷ cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not lower than 1 x 10¹⁶ cm⁻³, and oxygen at a concentration not lower than 1 x 10¹⁷ cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 80, Zhang¹ discloses in figure 4c a thin film transistor. Zhang¹ discloses in figures 1a - 1c, 2a - 2d, and 4a - 4c a crystalline semiconductor island on an insulating surface. Zhang¹ discloses in figures 4b and 4c source and drain regions in said semiconductor island. Zhang¹ discloses in figure 4b a channel forming region between said source and drain regions. Zhang¹ discloses in figures 4a – 4c a gate insulating film on at least said channel forming region. Zhang¹ discloses in figures 1a- 1c, 2a - 2d, and 4a - 4c a gate electrode over said channel forming region having said gate insulating film therebetween, wherein said channel forming region has no grain boundary. Zhang¹ discloses in column 9, lines 38 – 45 a thin film transistor wherein said semiconductor island includes the point defect (oxygen) of less 1 x 10¹⁸ cm⁻³. It is not clear if Zhang¹ teaches wherein said semiconductor island includes a point defect of 1 x 10¹⁶ cm⁻³ or more. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to have said semiconductor island include a point defect of 1 x 10¹⁶ cm⁻³ or more in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. Zhang¹ discloses in figures 4c and column11, lines 47 – 56 wherein said crystalline semiconductor island includes

hydrogen at concentration less than 1 x 10²⁰ cm⁻³ (i.e. the known atomic density of Si is 10²² cm⁻³, less than 5% of 10²² is less than 10²⁰). It is not clear if Zhang¹ teaches that the hydrogen concentration is not higher than 1 x 10²⁰ cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the hydrogen atom concentration of not higher than 1 x 10²⁰ cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. Further, any changes in particular device concentrations or properties would have been routine experimentation for one of ordinary skill seeking to maximize device function in the device of Zhang¹.

With regard to claim 81, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 wherein the crystalline semiconductor island comprise a material of Ni.

With regard to claim 82, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19-40 a thin film transistor wherein said material is included in said semiconductor island at a concentration less than 5×10^{19} cm⁻³. It is not clear if Zhang¹ teaches that the material is included in the semiconductor at a concentration not higher than 5×10^{19} cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the material included in the semiconductor at a concentration not higher than 5×10^{19} cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 83, Zhang¹ discloses in figures 4c and column11, lines 47 – 56 wherein said semiconductor island includes hydrogen for neutralizing the point defect at a concentration less than 1×10^{20} cm⁻³. It is not clear if Zhang¹ teaches that the hydrogen concentration is not lower than 1×10^{-15} cm⁻³. MPEP 2144.05 states that overlapping ranges are

Art Unit: 2815

obvious. It would have been obvious to one of ordinary skill in the art to use the hydrogen atom concentration of not lower than 1×10^{-15} cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 84, No differences have been pointed out in the formation of the channel forming region of Zhang¹ and the channel forming region of the current pending claim in view of the currently pending specification. Therefore Zhang¹ must teach in figures 1a - 1c, 2a - 2d and 4a - 4c wherein said semiconductor island includes a spin density of 1×10^{15} to 1×10^{17} cm⁻³, because an identical spin density is a property that must be shared by products that result from two processes that are the same.

With regard to claim 85, Zhang¹ discloses in column 4, lines 18 – 20 wherein said semiconductor island is a silicon island.

With regard to claim 86, Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than 1 x 10^{18} cm⁻³, and oxygen at a concentration less than 1 x 10^{18} cm⁻³. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not lower than 1 x 10^{16} cm⁻³, and oxygen at a concentration not lower than 1 x 10^{17} cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not lower than 1 x 10^{16} cm⁻³, and oxygen at a concentration not lower than 1 x 10^{17} cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 87, Zhang¹ discloses in figure 4c a semiconductor device. Zhang¹ discloses in figures 1a – 1c, 2a - 2d, and 4a – 4c a crystalline semiconductor island on an

insulating surface. Zhang¹ discloses in figures 4b and 4c source and drain regions in said semiconductor island. Zhang¹ discloses in figure 4b a channel forming region between said source and drain regions. Zhang¹ discloses in figures 1a-1c, 2a - 2d, and 4a - 4c a gate insulating film adjacent to at least said channel forming region. Zhang¹ discloses in figures 1a-1c, 2a - 2d, and 4a - 4c a gate electrode adjacent to said channel forming region having said gate insulating film therebetween, wherein said crystalline semiconductor island is formed in a monodomain region which contains no grain boundary. Zhang¹ discloses in figures 4c and column 11, lines 47 – 56 wherein said crystalline semiconductor island includes hydrogen at concentration less than 1 x 10²⁰ cm⁻³ (i.e. the known atomic density of Si is 10²² cm⁻³, less than 5% of 10²² is less than 10²⁰). It is not clear if Zhang¹ teaches that the hydrogen concentration is not higher than 1×10^{20} cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the hydrogen atom concentration of not higher than 1 x 10²⁰ cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. Zhang¹ discloses in column 9, lines 38 - 45 wherein the semiconductor device includes a p-channel thin film transistor having a mobility in a range of $20 - 100 \text{ cm}^2/\text{Vs}$. Zhang is silent to the fact that the semiconductor device includes a p-channel thin film transistor having mobility in a range of 200-400 cm²/Vs. Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04. VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free p-channel monodomain region with mobility in a range of 200-400 cm²/Vs in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and that fact that the monodomain region of Zhang¹ has the same utility as that

Art Unit: 2815

of the claimed invention. Further, any changes in particular device concentrations or properties would have been routine experimentation for one of ordinary skill seeking to maximize device function in the device of Zhang¹.

With regard to claim 88, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 wherein said crystalline semiconductor island comprises a material Ni.

With regard to claim 89, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19-40 a thin film transistor wherein said material is included in said semiconductor island at a concentration less than 5×10^{19} cm⁻³. It is not clear if Zhang¹ teaches that the material is included in the semiconductor at a concentration not higher than 5×10^{19} cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the material included in the semiconductor at a concentration not higher than 5×10^{19} cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 90, Zhang¹ discloses in column 4, lines 18 – 20 wherein said semiconductor island is a silicon island.

With regard to claim 91, Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than 1 x 10^{18} cm⁻³, and oxygen at a concentration less than 1 x 10^{18} cm⁻³. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not lower than 1 x 10^{16} cm⁻³, and oxygen at a concentration not lower than 1 x 10^{17} cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not lower than 1 x 10^{16} cm⁻³.

³, and oxygen at a concentration not lower than 1 x 10¹⁷ cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 92, Zhang¹ teaches in figures 1a, 1b, 2a - 2d; and column 12, lines 1 -30 wherein said monodomain region has a grain size of 50 μ m or more. It should be noted that the crystal grains (3) grown around metal portions (2) must have a grain size of $50 - 100 \mu$ m when the metal portions are set from $25 - 50 \mu$ m apart as disclosed by Zhang¹ in column 12, lines 1 - 30.

With regard to claim 93, Zhang¹ discloses in figure 4c semiconductor device. Zhang¹ discloses in figures 1a - 1c, 2a - 2d, and 4a - 4c a crystalline semiconductor island on an insulating surface. Zhang¹ discloses in figures 4b and 4c source and drain regions in said semiconductor island. Zhang¹ discloses in figure 4b a channel forming region between said source and drain regions. Zhang 1 discloses in figures 1a- 1c, 2a - 2d, and 4a - 4c a gate insulating film adjacent to at least said channel forming region. Zhang¹ discloses in figures 1a-1c, 2a - 2d, and 4a - 4c a gate electrode adjacent to said channel forming region having said gate insulating film therebetween, wherein said channel forming region is formed in a monodomain region which contains no grain boundary. Zhang¹ discloses in figures 4c and column11, lines 47 - 56 wherein said crystalline semiconductor island includes hydrogen at concentration less than I x 10²⁰ cm⁻³ (i.e. the known atomic density of Si is 10²² cm⁻³, less than 5% of 10²² is less than 10^{20}). It is not clear if Zhang¹ teaches that the hydrogen concentration is not higher than 1×10^{20} cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the hydrogen atom concentration of not higher than 1×10^{20} cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹

overlap. Zhang¹ discloses in column 9, lines 38 – 45 wherein the semiconductor device includes a n-channel thin film transistor having a mobility in a range of 30 – 150 cm²/Vs. Zhang¹ is silent to the fact that the semiconductor device includes an n-channel thin film transistor having mobility in a range of 500-1000 cm²/Vs. Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free n-channel monodomain region with mobility in a range of 500-1000 cm²/Vs in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and that fact that the monodomain region of Zhang¹ has the same utility as that of the claimed invention. Further, any changes in particular device concentrations or properties would have been routine experimentation for one of ordinary skill seeking to maximize device function in the device of Zhang¹.

With regard to claim 94, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 wherein said crystalline semiconductor island comprises a material Ni.

With regard to claim 95, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19-40 a thin film transistor wherein said material is included in said semiconductor island at a concentration less than 5×10^{19} cm⁻³. It is not clear if Zhang¹ teaches that the material is included in the semiconductor at a concentration not higher than 5×10^{19} cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the material included in the semiconductor at a concentration not higher than 5×10^{19} cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

Art Unit: 2815

With regard to claim 96, Zhang¹ discloses in column 4, lines 18 – 20 wherein said semiconductor island is a silicon island.

With regard to claim 97, Zhang¹ discloses in column 9, lines 38 - 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not lower than $1 \times 10^{16} \text{ cm}^{-3}$, and oxygen at a concentration not lower than $1 \times 10^{17} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not lower than $1 \times 10^{16} \text{ cm}^{-3}$, and oxygen at a concentration not lower than $1 \times 10^{16} \text{ cm}^{-3}$, and oxygen at a concentration not lower than $1 \times 10^{16} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 98, Zhang¹ teaches in figures 1a, 1b, 2a - 2d; and column 12, lines 1 -30 wherein said monodomain region has a grain size of 50 μ m or more. It should be noted that the crystal grains (3) grown around metal portions (2) must have a grain size of $50 - 100 \mu$ m when the metal portions are set from $25 - 50 \mu$ m apart as disclosed by Zhang¹ in column 12, lines 1 - 30.

With regard to claim 99, Zhang¹ discloses in figure 4c semiconductor device. Zhang¹ discloses in column 9, lines 38 – 45 a p-channel thin film transistor. Zhang¹ discloses in column 9, lines 38 – 45 an n-channel thin film transistor. Zhang¹ discloses in figures 1a – 1c, 2a - 2d, and 4a – 4c a crystalline semiconductor island on an insulating surface. Zhang¹ discloses in figures 4b and 4c source and drain regions in said semiconductor island. Zhang¹ discloses in figure 4b a channel forming region between said source and drain regions. Zhang¹ discloses in figures 1a-

1c, 2a – 2d, and 4a – 4c a gate insulating film adjacent to at least said channel forming region. Zhang¹ discloses in figures 1a- 1c, 2a – 2d, and 4a – 4c a gate electrode adjacent to said channel forming region having said gate insulating film therebetween, wherein said crystalline semiconductor island is formed in a monodomain region which contains no grain boundary. Zhang¹ discloses in figures 4c and column11, lines 47 – 56 wherein said crystalline semiconductor island includes hydrogen at concentration less than 1 x 10²0 cm⁻³ (i.e. the known atomic density of Si is 10²² cm⁻³, less than 5% of 10²² is less than 10²⁰). It is not clear if Zhang¹ teaches that the hydrogen concentration is not higher than 1 x 10²⁰ cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the hydrogen atom concentration of not higher than 1 x 10²⁰ cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. Further, any changes in particular device concentrations or properties would have been routine experimentation for one of ordinary skill seeking to maximize device function in the device of Zhang¹.

With regard to claim 100, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 wherein said crystalline semiconductor island comprises a material Ni.

With regard to claim 101, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19-40 a thin film transistor wherein said material is included in said semiconductor island at a concentration less than 5×10^{19} cm⁻³. It is not clear if Zhang¹ teaches that the material is included in the semiconductor at a concentration not higher than 5×10^{19} cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the material included in the semiconductor at a concentration not higher than 5×10^{19} cm⁻³ in

Art Unit: 2815

the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 102, Zhang¹ discloses in column 4, lines 18 – 20 wherein said semiconductor island is a silicon island.

With regard to claim 103, Zhang¹ discloses in column 9, lines 38 - 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than 1 x 10^{18} cm⁻³, and oxygen at a concentration less than 1 x 10^{18} cm⁻³. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not lower than 1 x 10^{16} cm⁻³, and oxygen at a concentration not lower than 1 x 10^{17} cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not lower than 1 x 10^{16} cm⁻³, and oxygen at a concentration not lower than 1 x 10^{17} cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 104, Zhang¹ teaches in figures 1a, 1b, 2a-2d; and column 12, lines 1-30 wherein said monodomain region has a grain size of 50 μ m or more. It should be noted that the crystal grains (3) grown around metal portions (2) must have a grain size of 50-100 μ m when the metal portions are set from 25-50 μ m apart as disclosed by Zhang¹ in column 12, lines 1-30.

With regard to claim 105, Zhang¹ discloses in figure 4c semiconductor device. Zhang¹ discloses in column 9, lines 38 – 45 a p-channel thin film transistor. Zhang¹ discloses in column 9, lines 38 – 45 an n-channel thin film transistor. Zhang¹ discloses in figures 1a – 1c, 2a - 2d, and 4a – 4c a crystalline semiconductor island on an insulating surface. Zhang¹ discloses in figures

Art Unit: 2815

4b and 4c source and drain regions in said semiconductor island. Zhang¹ discloses in figure 4b a channel forming region between said source and drain regions. Zhang discloses in figures 1a-1c, 2a – 2d, and 4a – 4c a gate insulating film adjacent to at least said channel forming region. Zhang¹ discloses in figures 1a - 1c, 2a - 2d, and 4a - 4c a gate electrode adjacent to said channel forming region having said gate insulating film therebetween, wherein said channel forming region is formed in a monodomain region which contains no grain boundary. Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon at a concentration less than 1×10^{18} cm⁻³. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon at a concentration not higher than 5 x 10¹⁸ cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon at a concentration not higher than 5×10^{18} cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. Zhang¹ discloses in figures 4c and column11, lines 47 – 56 wherein said crystalline semiconductor island includes hydrogen at concentration less than 1 x 10²⁰ cm⁻³ (i.e. the known atomic density of Si is 10^{22} cm⁻³, less than 5% of 10^{22} is less than 10^{20}). It is not clear if Zhang¹ teaches that the hydrogen concentration is not higher than 1 x 10²⁰ cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the hydrogen atom concentration of not higher than 1 x 10²⁰ cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. Further, any changes in particular device concentrations or properties would have been routine experimentation for one of ordinary skill seeking to maximize device function in the device of Zhang¹.

With regard to claim 106, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 wherein said crystalline semiconductor island comprises a material Ni.

With regard to claim 107, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19-40 a thin film transistor wherein said material is included in said semiconductor island at a concentration less than 5×10^{19} cm⁻³. It is not clear if Zhang¹ teaches that the material is included in the semiconductor at a concentration not higher than 5×10^{19} cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the material included in the semiconductor at a concentration not higher than 5×10^{19} cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 108, Zhang¹ discloses in column 4, lines 18 – 20 wherein said semiconductor island is a silicon island.

With regard to claim 109, Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than 1 x 10¹⁸ cm⁻³, and oxygen at a concentration less than 1 x 10¹⁸ cm⁻³. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not lower than 1 x 10¹⁶ cm⁻³, and oxygen at a concentration not lower than 1 x 10¹⁷ cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not lower than 1 x 10¹⁶ cm⁻³, and oxygen at a concentration not lower than 1 x 10¹⁷ cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 110, Zhang¹ teaches in figures 1a, 1b, 2a-2d; and column 12, lines 1-30 wherein said monodomain region has a grain size of 50 μ m or more. It should be noted that the crystal grains (3) grown around metal portions (2) must have a grain size of 50-100 μ m when the metal portions are set from 25-50 μ m apart as disclosed by Zhang¹ in column 12, lines 1-30.

With regard to claim 111, Zhang¹ discloses in figure 4c semiconductor device. Zhang¹ discloses in figures 8a and 8b; and column 9, lines 28 - 37 an active matrix circuit portion including at least a first thin film transistor. Zhang¹ discloses in column 9, lines 38 – 45 a driving circuit portion including at least a second thin film transistor. Zhang discloses in figures 1a - 1c, 2a - 2d, and 4a - 4c a crystalline semiconductor island on an insulating surface. Zhang discloses in figures 4b and 4c source and drain regions in said semiconductor island. Zhang¹ discloses in figure 4b a channel forming region between said source and drain regions. Zhang¹ discloses in figures 1a-1c, 2a-2d, and 4a-4c a gate insulating film adjacent to at least said channel forming region. Zhang¹ discloses in figures 1a- 1c, 2a - 2d, and 4a - 4c a gate electrode adjacent to said channel forming region having said gate insulating film therebetween, wherein said crystalline semiconductor island is formed in a monodomain region which contains no grain boundary. Zhang¹ discloses in figures 4c and column11, lines 47 – 56 wherein said crystalline semiconductor island includes hydrogen at concentration less than 1 x 10²⁰ cm⁻³ (i.e. the known atomic density of Si is 10^{22} cm⁻³, less than 5% of 10^{22} is less than 10^{20}). It is not clear if Zhang¹ teaches that the hydrogen concentration is not higher than 1 x 10²⁰ cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the hydrogen atom concentration of not higher than 1 x 10²⁰ cm⁻³ in the device of

Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. Further, any changes in particular device concentrations or properties would have been routine experimentation for one of ordinary skill seeking to maximize device function in the device of Zhang¹.

With regard to claim 112, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 wherein said crystalline semiconductor island comprises a material Ni.

With regard to claim 113, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 - 40 a thin film transistor wherein said material is included in said semiconductor island at a concentration less than 5 x 10¹⁹ cm⁻³. It is not clear if Zhang¹ teaches that the material is included in the semiconductor at a concentration not higher than 5 x 10¹⁹ cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the material included in the semiconductor at a concentration not higher than 5 x 10¹⁹ cm⁻³in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 114, Zhang¹ discloses in column 4, lines 18 – 20 wherein said semiconductor island is a silicon island.

With regard to claim 115, Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than 1 x 10¹⁸ cm⁻³, and oxygen at a concentration less than 1 x 10¹⁸ cm⁻³. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not lower than 1 x 10¹⁶ cm⁻³, and oxygen at a concentration not lower than 1 x 10¹⁷ cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of

ordinary skill in the art to use carbon and nitrogen at a concentration not lower than 1×10^{16} cm⁻³, and oxygen at a concentration not lower than 1×10^{17} cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 116, Zhang¹ teaches in figures 1a, 1b, 2a-2d; and column 12, lines 1-30 wherein said monodomain region has a grain size of 50 μ m or more. It should be noted that the crystal grains (3) grown around metal portions (2) must have a grain size of 50-100 μ m when the metal portions are set from 25-50 μ m apart as disclosed by Zhang¹ in column 12, lines 1-30.

With regard to claim 123, Zhang¹ discloses in figure 4c a semiconductor device. Zhang¹ discloses in figures 1a – 1c, 2a - 2d, and 4a – 4c a crystalline semiconductor island on an insulating surface. Zhang¹ discloses in figures 4b and 4c source and drain regions in said semiconductor island. Zhang¹ discloses in figure 4b a channel forming region between said source and drain regions. Zhang¹ discloses in figures 1a- 1c, 2a – 2d, and 4a – 4c a gate insulating film adjacent to at least said channel forming region. Zhang¹ discloses in figures 1a- 1c, 2a – 2d, and 4a – 4c a gate electrode adjacent to said channel forming region having said gate insulating film therebetween, wherein said crystalline semiconductor island is formed in a monodomain region which contains no grain boundary. Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than 1 x 10¹8 cm⁻³. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not higher than 5 x 10¹8 cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not higher than 5 x 10¹8 cm⁻³ in the device of

Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. No differences have been pointed out in the formation of the channel forming region of Zhang¹ and the channel forming region of the current pending claim in view of the currently pending specification. Therefore Zhang¹ must teach in figures 1a - 1c, 2a - 2d and 4a - 4c wherein said semiconductor device has a S value of 0.03-0.3, because an identical S value is a property that must be shared by products that result from two processes that are the same. Zhang¹ discloses in figures 4c and column 11, lines 47 – 56 wherein said crystalline semiconductor island includes hydrogen at concentration less than 1×10^{20} cm⁻³ (i.e. the known atomic density of Si is 10^{22} cm⁻³, less than 5% of 10²² is less than 10²⁰). It is not clear if Zhang¹ teaches that the hydrogen concentration is not higher than 1 x 10²⁰ cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the hydrogen atom concentration of not higher than 1 x 10²⁰ cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. Zhang¹ discloses in column 9, lines 38 - 45 wherein the semiconductor device includes at least one selected from the group consisting of a p-channel thin film transistor and an n-channel thin film transistor. Zhang¹ discloses in column 9, lines 38 – 45 wherein the semiconductor device includes a p-channel thin film transistor having a mobility in a range of 20 – 100 cm²/Vs. Zhang¹ is silent to the fact that the semiconductor device includes a p-channel thin film transistor having mobility in a range of 200-400 cm²/Vs. Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free p-channel monodomain region with mobility in a range of 200-400 cm²/Vs in the device of Zhang¹. This is because the prior art teaches a suitable

method for obtaining the claimed mobility, and that fact that the monodomain region of Zhang¹ has the same utility as that of the claimed invention. Zhang¹ discloses in column 9, lines 38 – 45 wherein the semiconductor device includes an n-channel thin film transistor having a mobility in a range of 30 – 150 cm²/Vs. Zhang¹ is silent to the fact that the semiconductor device includes an n-channel thin film transistor having mobility in a range of 500-1000 cm²/Vs. Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free n-channel monodomain region with mobility in a range of 500-1000 cm²/Vs in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and that fact that the monodomain region of Zhang¹ has the same utility as that of the claimed invention. Further, any changes in particular device concentrations or properties would have been routine experimentation for one of ordinary skill seeking to maximize device function in the device of Zhang¹.

With regard to claim 124, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 wherein said crystalline semiconductor island comprises a material Ni.

With regard to claim 125, Zhang¹ discloses in figures 1a, 1b, and 2a, and column 6, lines 19-40 a thin film transistor wherein said material is included in said semiconductor island at a concentration less than 5×10^{19} cm⁻³. It is not clear if Zhang¹ teaches that the material is included in the semiconductor at a concentration not higher than 5×10^{19} cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the material included in the semiconductor at a concentration not higher than 5×10^{19} cm⁻³ in

Art Unit: 2815

the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 126, Zhang¹ discloses in column 4, lines 18 – 20 wherein said semiconductor island is a silicon island.

With regard to claim 127, Zhang¹ discloses in column 9, lines 38 - 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than 1 x 10^{18} cm⁻³, and oxygen at a concentration less than 1 x 10^{18} cm⁻³. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not lower than 1 x 10^{16} cm⁻³, and oxygen at a concentration not lower than 1 x 10^{17} cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not lower than 1 x 10^{16} cm⁻³, and oxygen at a concentration not lower than 1 x 10^{17} cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 128, Zhang¹ teaches in figures 1a, 1b, 2a-2d; and column 12, lines 1-30 wherein said monodomain region has a grain size of 50 μ m or more. It should be noted that the crystal grains (3) grown around metal portions (2) must have a grain size of 50-100 μ m when the metal portions are set from 25-50 μ m apart as disclosed by Zhang¹ in column 12, lines 1-30.

With regard to claim 129, Zhang¹ discloses in figure 4c semiconductor device. Zhang¹ discloses in figures 1a – 1c, 2a - 2d, and 4a – 4c a crystalline semiconductor island on an insulating surface. Zhang¹ discloses in figures 4b and 4c source and drain regions in said semiconductor island. Zhang¹ discloses in figure 4b a channel forming region between said

source and drain regions. Zhang¹ discloses in figures 1a- 1c, 2a - 2d, and 4a - 4c a gate insulating film adjacent to at least said channel forming region. Zhang discloses in figures 1a-1c, 2a - 2d, and 4a - 4c a gate electrode adjacent to said channel forming region having said gate insulating film therebetween, wherein said crystalline semiconductor island includes carbon and nitrogen at a concentration not higher than 5 x 1018 cm-3, wherein said channel forming region is formed in a monodomain region which contains no grain boundary. No differences have been pointed out in the formation of the channel forming region of Zhang¹ and the channel forming region of the current pending claim in view of the currently pending specification. Therefore Zhang¹ must teach in figures 1a - 1c, 2a - 2d and 4a - 4c wherein said semiconductor device has a S value of 0.03-0.3, because an identical S value is a property that must be shared by products that result from two processes that are the same. Zhang¹ discloses in figures 4c and column11, lines 47 – 56 wherein said crystalline semiconductor island includes hydrogen at concentration less than 1×10^{20} cm⁻³ (i.e. the known atomic density of Si is 10^{22} cm⁻³, less than 5% of 10^{22} is less than 10²⁰). It is not clear if Zhang¹ teaches that the hydrogen concentration is not higher than 1 x 10²⁰ cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the hydrogen atom concentration of not higher than 1 x 10²⁰ cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. Zhang¹ discloses in column 9, lines 38 – 45 wherein the semiconductor device includes at least one selected from the group consisting of a p-channel thin film transistor and an n-channel thin film transistor. Zhang¹ discloses in column 9, lines 38 – 45 wherein the semiconductor device includes a p-channel thin film transistor having a mobility in a range of 20 - 100 cm²/Vs. Zhang¹ is silent to the fact that the semiconductor device includes a p-channel

thin film transistor having mobility in a range of 200-400 cm²/Vs. Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free p-channel monodomain region with mobility in a range of 200-400 cm²/Vs in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and that fact that the monodomain region of Zhang¹ has the same utility as that of the claimed invention. Zhang¹ discloses in column 9, lines 38 – 45 wherein the semiconductor device includes an nchannel thin film transistor having a mobility in a range of 30 – 150 cm²/Vs. Zhang¹ is silent to the fact that the semiconductor device includes an n-channel thin film transistor having mobility in a range of 500-1000 cm²/Vs. Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free n-channel monodomain region with mobility in a range of 500-1000 cm²/Vs in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and that fact that the monodomain region of Zhang¹ has the same utility as that of the claimed invention. Further, any changes in particular device concentrations or properties would have been routine experimentation for one of ordinary skill seeking to maximize device function in the device of Zhang¹.

With regard to claim 130, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 wherein said crystalline semiconductor island comprises a material Ni.

With regard to claim 131, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19-40 a thin film transistor wherein said material is included in said semiconductor island at a concentration less than 5×10^{19} cm⁻³. It is not clear if Zhang¹ teaches that the material is included

in the semiconductor at a concentration not higher than 5×10^{19} cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the material included in the semiconductor at a concentration not higher than 5×10^{19} cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 132, Zhang¹ discloses in column 4, lines 18 – 20 wherein said semiconductor island is a silicon island.

With regard to claim 133, Zhang¹ discloses in column 9, lines 38 - 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than 1 x 10^{18} cm⁻³, and oxygen at a concentration less than 1 x 10^{18} cm⁻³. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not lower than 1 x 10^{16} cm⁻³, and oxygen at a concentration not lower than 1 x 10^{17} cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not lower than 1 x 10^{16} cm⁻³, and oxygen at a concentration not lower than 1 x 10^{17} cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 134, Zhang¹ teaches in figures 1a, 1b, 2a - 2d; and column 12, lines 1 - 30 wherein said monodomain region has a grain size of 50 μ m or more. It should be noted that the crystal grains (3) grown around metal portions (2) must have a grain size of $50 - 100 \mu$ m when the metal portions are set from $25 - 50 \mu$ m apart as disclosed by Zhang¹ in column 12, lines 1 - 30.

Art Unit: 2815

With regard to claim 135, Zhang¹ discloses in column 9, lines 38 – 45 wherein each of the concentrations of carbon, nitrogen and oxygen is measured. A method of measuring does not define a patentable feature in a device claim. Therefore, measuring the concentration of carbon, nitrogen and oxygen by the well known technique of secondary ion mass spectroscopy (SIMS) does not bear any patentable weight in this device claim. Further, it would have been obvious to one of ordinary skill in the art at the time of the present invention to use the technique of SIMS to measure the concentration of carbon, nitrogen and oxygen in the device of Zhang¹ in order to understand the electrical characteristics of the device as it relates to these features. This understanding would result in better device control.

With regard to claim 136, Zhang¹ discloses in column 9, lines 38 – 45 wherein each of the concentrations of carbon, nitrogen and oxygen is measured. A method of measuring does not define a patentable feature in a device claim. Therefore, measuring the concentration of carbon, nitrogen and oxygen by the well known technique of secondary ion mass spectroscopy (SIMS) does not bear any patentable weight in this device claim. Further, it would have been obvious to one of ordinary skill in the art at the time of the present invention to use the technique of SIMS to measure the concentration of carbon, nitrogen and oxygen in the device of Zhang¹ in order to understand the electrical characteristics of the device as it relates to these features. This understanding would result in better device control.

With regard to claim 137, Zhang¹ discloses in column 9, lines 38 – 45 wherein each of the concentrations of carbon, nitrogen and oxygen is measured. A method of measuring does not define a patentable feature in a device claim. Therefore, measuring the concentration of carbon, nitrogen and oxygen by the well known technique of secondary ion mass spectroscopy

(SIMS) does not bear any patentable weight in this device claim. Further, it would have been obvious to one of ordinary skill in the art at the time of the present invention to use the technique of SIMS to measure the concentration of carbon, nitrogen and oxygen in the device of Zhang¹ in order to understand the electrical characteristics of the device as it relates to these features. This understanding would result in better device control.

With regard to claim 138, Zhang¹ discloses in column 9, lines 38 – 45 wherein each of the concentrations of carbon, nitrogen and oxygen is measured. A method of measuring does not define a patentable feature in a device claim. Therefore, measuring the concentration of carbon, nitrogen and oxygen by the well known technique of secondary ion mass spectroscopy (SIMS) does not bear any patentable weight in this device claim. Further, it would have been obvious to one of ordinary skill in the art at the time of the present invention to use the technique of SIMS to measure the concentration of carbon, nitrogen and oxygen in the device of Zhang¹ in order to understand the electrical characteristics of the device as it relates to these features. This understanding would result in better device control.

With regard to claim 139, Zhang¹ discloses in column 9, lines 38 – 45 wherein each of the concentrations of carbon, nitrogen and oxygen is measured. A method of measuring does not define a patentable feature in a device claim. Therefore, measuring the concentration of carbon, nitrogen and oxygen by the well known technique of secondary ion mass spectroscopy (SIMS) does not bear any patentable weight in this device claim. Further, it would have been obvious to one of ordinary skill in the art at the time of the present invention to use the technique of SIMS to measure the concentration of carbon, nitrogen and oxygen in the device of Zhang¹ in order to

understand the electrical characteristics of the device as it relates to these features. This understanding would result in better device control.

With regard to claim 140, Zhang¹ discloses in column 9, lines 38 – 45 wherein each of the concentrations of carbon, nitrogen and oxygen is measured. A method of measuring does not define a patentable feature in a device claim. Therefore, measuring the concentration of carbon, nitrogen and oxygen by the well known technique of secondary ion mass spectroscopy (SIMS) does not bear any patentable weight in this device claim. Further, it would have been obvious to one of ordinary skill in the art at the time of the present invention to use the technique of SIMS to measure the concentration of carbon, nitrogen and oxygen in the device of Zhang¹ in order to understand the electrical characteristics of the device as it relates to these features. This understanding would result in better device control.

With regard to claim 141, Zhang¹ discloses in column 9, lines 38 – 45 wherein each of the concentrations of carbon, nitrogen and oxygen is measured. A method of measuring does not define a patentable feature in a device claim. Therefore, measuring the concentration of carbon, nitrogen and oxygen by the well known technique of secondary ion mass spectroscopy (SIMS) does not bear any patentable weight in this device claim. Further, it would have been obvious to one of ordinary skill in the art at the time of the present invention to use the technique of SIMS to measure the concentration of carbon, nitrogen and oxygen in the device of Zhang¹ in order to understand the electrical characteristics of the device as it relates to these features. This understanding would result in better device control.

With regard to claim 143, Zhang¹ discloses in column 9, lines 38 – 45 wherein each of the concentrations of carbon, nitrogen and oxygen is measured. A method of measuring does not

define a patentable feature in a device claim. Therefore, measuring the concentration of carbon, nitrogen and oxygen by the well known technique of secondary ion mass spectroscopy (SIMS) does not bear any patentable weight in this device claim. Further, it would have been obvious to one of ordinary skill in the art at the time of the present invention to use the technique of SIMS to measure the concentration of carbon, nitrogen and oxygen in the device of Zhang¹ in order to understand the electrical characteristics of the device as it relates to these features. This understanding would result in better device control.

With regard to claim 144, Zhang¹ discloses in column 9, lines 38 – 45 wherein each of the concentrations of carbon, nitrogen and oxygen is measured. A method of measuring does not define a patentable feature in a device claim. Therefore, measuring the concentration of carbon, nitrogen and oxygen by the well known technique of secondary ion mass spectroscopy (SIMS) does not bear any patentable weight in this device claim. Further, it would have been obvious to one of ordinary skill in the art at the time of the present invention to use the technique of SIMS to measure the concentration of carbon, nitrogen and oxygen in the device of Zhang¹ in order to understand the electrical characteristics of the device as it relates to these features. This understanding would result in better device control.

With regard to claim 145, Zhang¹ discloses in column 9, lines 38 - 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than 1 x 10^{18} cm⁻³, and oxygen at a concentration less than 1 x 10^{18} cm⁻³. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not higher than 5 x 10^{18} cm⁻³, and oxygen at a concentration not higher than 5 x 10^{19} cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of

ordinary skill in the art to use carbon and nitrogen at a concentration not higher than 5×10^{18} cm⁻³, and oxygen at a concentration not higher than 5×10^{19} cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 146, Zhang¹ discloses in column 9, lines 38 – 45 wherein the thin film transistor is an n-channel thin film transistor having a mobility in a range of 30 – 150 cm²/Vs. Zhang¹ is silent to the fact that the semiconductor device includes an n-channel thin film transistor having mobility in a range of 500-1000 cm²/Vs. Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free n-channel monodomain region with mobility in a range of 500-1000 cm²/Vs in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and that fact that the monodomain region of Zhang¹ has the same utility as that of the claimed invention.

With regard to claim 147, Zhang¹ discloses in column 9, lines 38 - 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than 1 x 10^{18} cm⁻³, and oxygen at a concentration less than 1 x 10^{18} cm⁻³. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not higher than 5 x 10^{18} cm⁻³, and oxygen at a concentration not higher than 5 x 10^{19} cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not higher than 5 x 10^{18} cm⁻³, and oxygen at a concentration not higher than 5 x 10^{19} cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

Art Unit: 2815

With regard to claim 148, Zhang¹ discloses in column 9, lines 38 – 45 wherein the thin film transistor is one of a p-channel thin film transistor having a mobility in a range of 20 – 100 cm²/Vs. Zhang¹ is silent to the fact that the semiconductor device includes a p-channel thin film transistor having mobility in a range of 200-400 cm²/Vs. Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free p-channel monodomain region with mobility in a range of 200-400 cm²/Vs in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and that fact that the monodomain region of Zhang¹ has the same utility as that of the claimed invention.

With regard to claim 149, Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than 1 x 10^{18} cm⁻³, and oxygen at a concentration less than 1 x 10^{18} cm⁻³. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not higher than 5 x 10^{18} cm⁻³, and oxygen at a concentration not higher than 5 x 10^{19} cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not higher than 5 x 10^{18} cm⁻³, and oxygen at a concentration not higher than 5 x 10^{19} cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 150, Zhang¹ discloses in column 9, lines 38 - 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than 1 x 10^{18} cm⁻³, and oxygen at a concentration less than 1 x 10^{18} cm⁻³. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not

higher than 5×10^{18} cm⁻³, and oxygen at a concentration not higher than 5×10^{19} cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not higher than 5×10^{18} cm⁻³, and oxygen at a concentration not higher than 5×10^{19} cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 151, Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than 1 x 10^{18} cm⁻³, and oxygen at a concentration less than 1 x 10^{18} cm⁻³. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not higher than 5 x 10^{18} cm⁻³, and oxygen at a concentration not higher than 5 x 10^{19} cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not higher than 5 x 10^{18} cm⁻³, and oxygen at a concentration not higher than 5 x 10^{19} cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 152, Zhang¹ discloses in column 9, lines 38 – 45 wherein the semiconductor device includes a p-channel thin film transistor having a mobility in a range of 20 – 100 cm²/Vs. Zhang¹ is silent to the fact that the semiconductor device includes a p-channel thin film transistor having mobility in a range of 200-400 cm²/Vs. Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free p-channel monodomain region with mobility in a range of 200-400 cm²/Vs in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and that fact

Art Unit: 2815

that the monodomain region of Zhang¹ has the same utility as that of the claimed invention. Zhang¹ discloses in column 9, lines 38 – 45 wherein the semiconductor device includes a n-channel thin film transistor having a mobility in a range of 30 – 150 cm²/Vs. Zhang¹ is silent to the fact that the semiconductor device includes an n-channel thin film transistor having mobility in a range of 500-1000 cm²/Vs. Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free n-channel monodomain region with mobility in a range of 500-1000 cm²/Vs in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and that fact that the monodomain region of Zhang¹ has the same utility as that of the claimed invention.

With regard to claim 153, Zhang¹ discloses in column 9, lines 38 – 45 wherein the semiconductor device includes a p-channel thin film transistor having a mobility in a range of 20 – 100 cm²/Vs. Zhang¹ is silent to the fact that the semiconductor device includes a p-channel thin film transistor having mobility in a range of 200-400 cm²/Vs. Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free p-channel monodomain region with mobility in a range of 200-400 cm²/Vs in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and that fact that the monodomain region of Zhang¹ has the same utility as that of the claimed invention. Zhang¹ discloses in column 9, lines 38 – 45 wherein the semiconductor device includes a n-channel thin film transistor having a mobility in a range of 30 – 150 cm²/Vs. Zhang¹ is silent to the fact that the semiconductor device includes an n-channel thin film transistor having mobility

Art Unit: 2815

in a range of 500-1000 cm²/Vs. Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free n-channel monodomain region with mobility in a range of 500-1000 cm²/Vs in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and that fact that the monodomain region of Zhang¹ has the same utility as that of the claimed invention.

With regard to claim 154, Zhang¹ discloses in column 9, lines 38 - 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than 1 x 10^{18} cm⁻³. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not higher than 5×10^{18} cm⁻³. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not higher than 5×10^{18} cm⁻³ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 155, Zhang¹ discloses in column 9, lines 38 – 45 wherein the second thin film transistor is one of a p-channel thin film transistor having a mobility in a range of 20 – 100 cm²/Vs. Zhang¹ is silent to the fact that the semiconductor device includes a p-channel thin film transistor having mobility in a range of 200-400 cm²/Vs. Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free p-channel monodomain region with mobility in a range of 200-400 cm²/Vs in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and

Application/Control Number: 08/520,079 Page 35

Art Unit: 2815

that fact that the monodomain region of Zhang¹ has the same utility as that of the claimed invention.

Response to Arguments

- 4. Applicant's arguments filed January 24, 2005 have been fully considered but they are not persuasive.
- 5. All responses to applicant's previous arguments are hereby incorporated from all former office actions.
- 6. With regard to applicant's argument that "the crystal grains of Zhang grow radially from the deposited metal, grain boundaries are generated along with the crystal growth as disclosed by Ohtani, and that a channel region includes grain boundaries," it should be noted that the rejection is not based on Ohtani. Further, Zhang clearly shows in figure 1c wherein the channel forming region has no grain boundary. In other words, Zhang shows in figure 1c wherein the channel forming region, under gate 7, on the islands 6, are not formed over the grain boundaries 4. Irrespective of how Ohtani forms grain boundaries, at the time of the present invention and Zhang one of ordinary skill would recognize only that no grain boundaries exist in the channel forming regions in the method of Zhang. Therefore applicant's arguments are not persuasive and the rejection is proper.

7. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Paul E. Brock II whose telephone number is (571) 272-1723. The examiner can normally be reached on 8:30 AM - 5:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on (571) 272-1664. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Art Unit: 2815

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Page 37

Paul E Brock II